

Curriculum Vitae for
Brian Michael Kurkoski
Japan Advanced Institute of Science and Technology
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Education

- **Ph.D.** 2004. University of California San Diego, Electrical Engineering (Communication Theory and Systems). Dissertation: *Algorithms and Schedules for Turbo Equalization* .
- **M.S.** 2000. University of California San Diego, Electrical Engineering.
- **B.S.** 1993 California Institute of Technology, Pasadena, California, Engineering and Applied Science (Electrical Engineering).

Academic and Research Positions

- **Associate Professor** Japan Advanced Institute of Science and Technology, Nomi, Japan. April 2012-present
- **Associate Professor** University of Electro-Communications (UEC), Tokyo, Japan. April 2007-March 2012
- **Research Associate** University of Electro-Communications (UEC), Tokyo, Japan. July 2006-March 2007
- **Postdoctoral Researcher** University of Electro-Communications (UEC), Tokyo, Japan. July 2004-June 2006
- **Visiting Researcher** Hitachi Central Research Laboratory, (June-December 2000)
- **Graduate Student Researcher** University of California, San Diego (UCSD), Center for Magnetic Recording Research. June 1999-June 2004

Professional Activities

- Guest Associate Editor for *IEICE Transactions* Special Section on Information Theory and Its Applications (2015, 2011)
- Associate editor for *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences* (2010-2014)
- Organizing activities:
 - Member, Conference Committee of the IEEE Information Theory Society, 2016-2018
 - Member, Board of Governors, IEICE Research Society of Information Theory and its Applications, 2015-2016
 - Secretary, Data Storage Technical Committee, IEEE Communications Society, 2013-2016

- Publicity Co-Chair, International Symposium on Information Theory and Its Applications (ISITA) 2014 and 2016
- Publicity Chair, IEEE Information Theory Workshop ITW 2014,
- Workshop on Coding for Flash Memories (Japan) 2010, 2012, 2013 [1]
- Technical program committee chair:
 - IEEE International Conference on Communications - Selected Areas in Communication, Data Storage 2014
 - International Conference on Computing, Networking and Communications ICNC 2013, Data Storage Technology and Applications, Symposium Co-Chair
- Technical program committee member:
 - IEEE Global Communications Conference (GLOBECOM) 2016, 2013, 2010, 2008
 - IEEE International Conference on Communications ICC
 - * Selected Areas in Communication Symposium (Data Storage Track) 2013
 - * Communication Theory Symposium: 2017, 2014
 - IEEE Global Conference on Signal and Information Processing, 2016
 - 9th International Symposium on Turbo Codes & Iterative Information Processing (ISTC), 2016.
 - IEEE Intermag, Technical Review Committee (2015 Recording System II Session, 2006)
 - Non-Volatile Memories Workshop (NVMW) 2014, 2013
 - International Symposium on Information Theory and Its Applications (ISITA) 2014, 2010
 - International Conference on Computing, Networking and Communications (ICNC) 2012
 - International Symposium on Communications & Information Technologies (ISCIT) 2011
- Reviewer for: *IEEE Transactions on Information Theory* , *IEEE Transactions on Communications* , *IEEE Transactions on Magnetics* , *IEICE Transactions* .
- Member of IEEE, IEICE.

Grants, Fellowships and Awards

- 2014-2016 Ministry of Education, Science, Sports and Culture Grant-in-Aid for Scientific Research (B), 26289119, “LatticeNET: Practical Lattice Codes for Cooperative Wireless Networks.” 12,400,000 yen (direct amount).
- 2012 IEICE Service Award. For service as an Editorial committee member of the English-language journal.
- 2011-2013 Ministry of Education, Science, Sports and Culture Grant-in-Aid for Scientific Research (C), 23560439, “Signal Processing and Coding Methods for High-Density Flash Memory Systems.” 4,000,000 yen (direct amount)
- 2007-2008 Ministry of Education, Science, Sports and Culture Grant-in-Aid for Scientific Research (C), 19560371, “Decoding Methods for Non-binary LDPC Codes Using Information Theoretic Approaches” 1,500,000 yen (direct amount)
- 2004-2005 JSPS Postdoctoral Fellowship. A two-year fellowship supported by the Japan Society for the Promotion of Science, number 00004908, 2004.

- 2004 Sheldon Schultz Prize for Excellence in Graduate Student Research. Center for Magnetic Recording Research, University of California, San Diego.
- 2000 Jacobs Fellowship. Department of Electrical and Computer Engineering, University of California, San Diego.

Biography

Brian M. Kurkoski is an Associate Professor at the Japan Advanced Institute of Science and Technology (JAIST) in Nomi, Japan. Born in Portland, Oregon, he received the B.S. degree from the California Institute of Technology in 1993, and then worked at two California startups. He received the M.S. and Ph.D. degrees from the University of California San Diego in 2000 and 2004, respectively. He received a JSPS Postdoctoral Fellowship from 2004 to 2006, while at the University of Electro-Communications in Tokyo, Japan, where he continued as Associate Professor from 2007 to 2012. He has been at JAIST since 2012. He was an associate editor for *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences* from 2010 to 2014. From 2013 to 2016, he was secretary of the Data Storage Technical Committee, a technical committee of the IEEE Communications Society. He is active in the organization of conferences and workshops on information theory and coding theory.